

IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant: **Doerr**

Case: **Doerr 74 (LCNT/125663)**

Serial No.: **10/657,863**

Filed: **September 9, 2003**

Examiner: **Jennifer Doan**

Group Art Unit: **2874**

Title: **INTEGRATEABLE OPTICAL INTERLEAVER AND DE-
INTERLEAVER**

COMMISSIONER OF PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

SIR:

DECLARATION UNDER 35 C.F.R. § 1.131

I, Christopher Richard Doerr, in support of conception and diligence in reduction to practice of claimed subject matter prior to filing of the present application on September 9, 2003, hereby declare as follows:

1. I invented the subject matter described and claimed in the present application filed on September 9, 2003 and am familiar with the disclosures and pending claims;
2. I conceived of the subject matter of all claims pending in this application prior to March 23, 2003, the publication date of *Doerr et al.*;
3. My conception of the claimed subject matter of the pending claims in this application prior to March 23, 2003 is evidenced by Exhibit A, which is an

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invention disclosure disclosing the claimed subject matter of the pending claims in this application;

4. That the invention disclosure shown in Exhibit A was prepared prior to March 23, 2003;

5. That I diligently pursued the subject matter of the pending claims from a time beginning before March 23, 2003, until filing of the present application on September 9, 2003, as further described in points 6 – 12 below;

6. That I submitted the invention disclosure shown in Exhibit A to my in-house legal department;

7. That my in-house legal department submitted the invention disclosure shown in Exhibit A to an outside legal firm for prosecution of the present invention on April 10, 2003, as evidenced in Exhibit B;

8. That the outside legal firm was diligently working on the present application prior to June 20, 2003 when the outside legal firm contacted me regarding questions with respect to drafting of the present application, as evidenced in Exhibit C;

9. That the outside legal firm provided a first draft of the present application to me on August 22, 2003, as evidenced by Exhibit D;

10. That additional communication regarding the present application was initiated by the outside legal firm on September 4, 2003, as evidenced by Exhibit E;

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11. That the outside legal firm provided a second final draft of the present application to me for acceptance of changes requested by my in-house legal department on September 5, 2003, as evidenced by Exhibit F;

12. That subsequent reduction to practice of the invention occurred at least on September 9, 2003 with the filing of the present application.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further, that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing therefrom.

2/7/06
Date


CHRISTOPHER RICHARD DOERR

Silica waveguide cross-connect-type wavelength add-drop with integrated interleavers

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IEEE indexing terms: Glass materials/devices, gratings, optical filter, optical phase shifters, wavelength-division multiplexing

Abstract

We employ silica waveguide technology to integrate two interleavers with a wavelength-selective cross-connect and a star coupler with variable optical attenuators, making a low-start-up-cost flexible add-drop node in a highly compact and fabrication-robust manner. To make the interleavers, we demonstrate a novel desensitized 2×2 coupler.

1. Design

To compete effectively with electronic solutions, wavelength-division multiplexed optical add-drop multiplexers (OADMs) need to have a high flexibility yet a low start-up cost. A start-up node is one that can drop only a subset of channels but maintains the total line capacity and is upgradeable to dropping more channels without complete line interruption. To achieve low cost we chose a simple, robust, all-solid-state technology like thermooptic silica waveguides and integrated all the routing elements onto one compact chip.

We designed a wavelength-selective-cross-connect (WSC)-type OADM node for a 16-channel 100-GHz-spacing wavelength-division multiplexed system. To keep the start-up cost low and yet the flexibility high, the system is divided into two 8-channel 200-GHz-spacing sets via interleavers. The start-up node is shown in Fig. 1a. The even-numbered channels can be dropped and added. The dropping is done via a 1×9 WSC (allowing each drop channel to appear at any port), and the adding is done via a 1×8 star coupler (allowing each add channel to be at any wavelength, assuming tunable transmitters) with variable optical attenuators (VOAs) and a coupler. When the user wishes to also drop and add the odd-numbered channels, a second WSC and a second coupler replace the attenuator in the odd-channel path. This can be done without disrupting the even-numbered channels. To make the system low cost, the de-interleaver, the WSC, the interleaver, and the 1×8 star coupler with VOAs are all integrated onto one silica waveguide planar lightwave circuit (PLC). These four components are left unconnected to each other on the PLC, in order to give the user as much flexibility as possible,

e.g., in case the user wishes the start-up node to drop and add the odd-numbered channels. Figure 1b shows how the node can be arranged in an East-West separable fashion using two of the PLCs, in order to preserve SONET 1:1 protection in the event of a PLC failure/replacement.

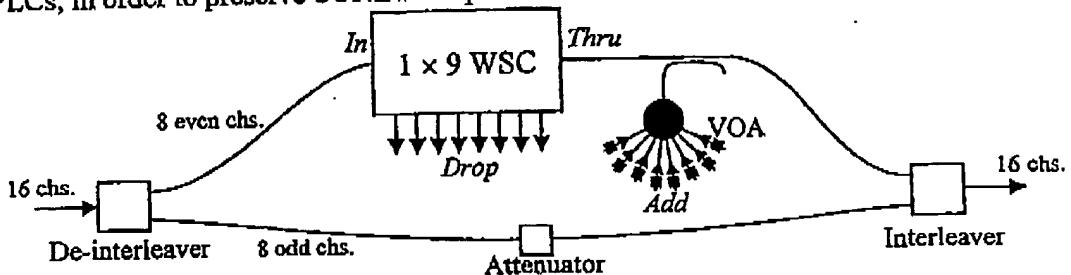


Fig. 1a. Block diagram of proposed low-cost start-up node for a 16-channel system. The de-interleaver, WSC, add 1×8 combiner and VOAs, and interleaver were all put on one PLC. To upgrade the node, a second PLC (identical except for a wavelength shift of 100 GHz) is added, replacing the attenuator with a second WSC and add-path coupler.

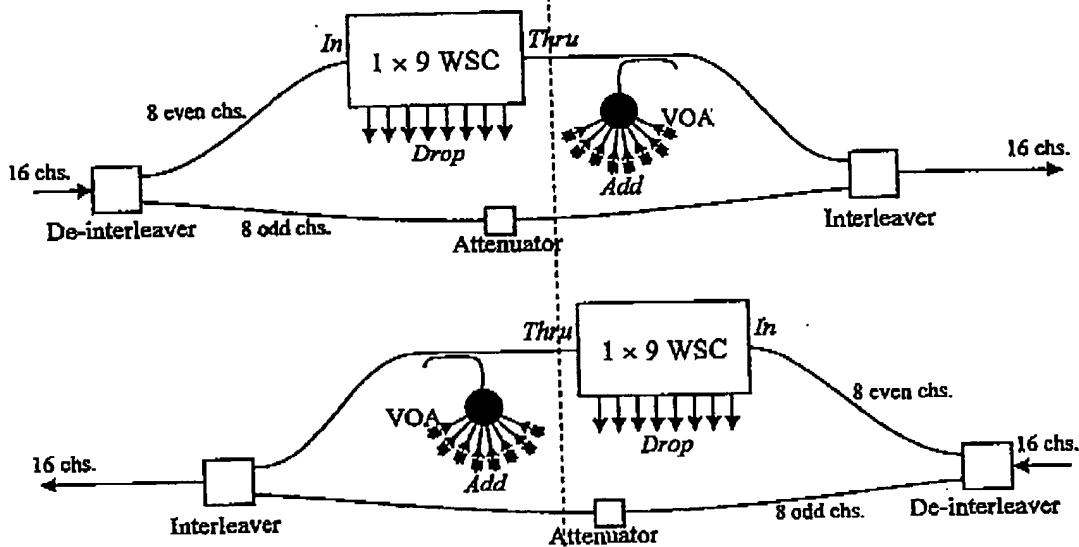


Fig. 1b. Block diagram of start-up node for both directions. To make the system East-West separable, only components from one side of the dotted line are integrated on the same PLC.

The PLC waveguide layout is shown in Fig. 2a. The WSC and add star with VOAs are nearly identical to that of Ref. [1]. The interleavers were squeezed into a small open space, resulting in no change in PLC size, and thus there are still three PLCs per 5" wafer. The interleavers are Fourier-filter type^[2], each consisting of a two-stage Mach-Zehnder interferometer (MZI). The interleavers have thermooptic trimmers on the MZI arms to adjust their phases. Because these integrated interleavers need to have a high yield and yet be compact, we used a y-branch coupler for the first coupler and novel three-stage couplers for the second two, as shown in Fig. 2b.

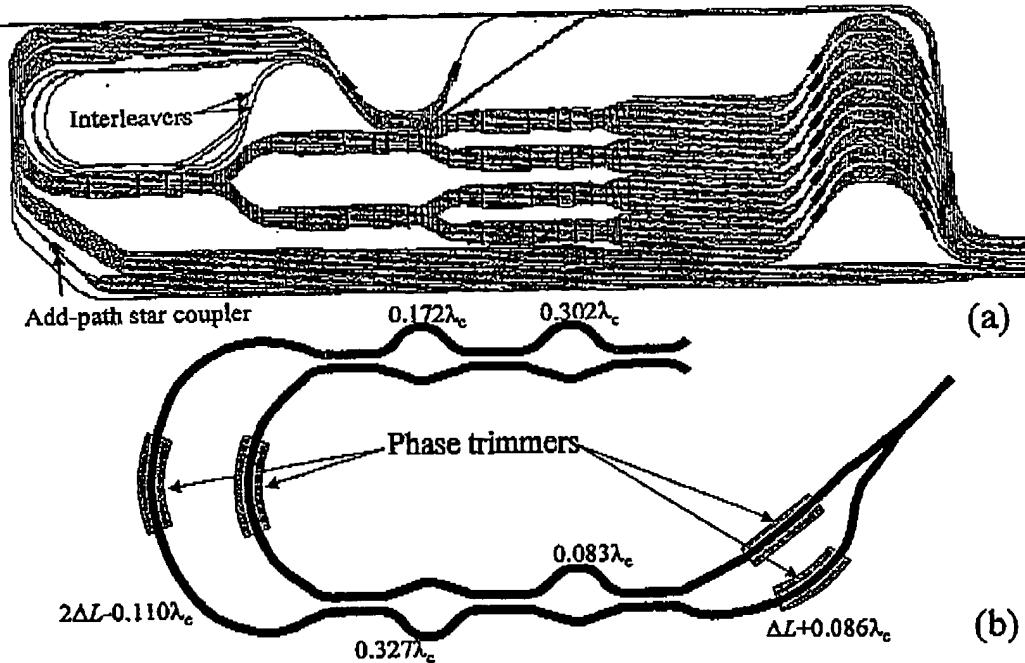


Fig. 2. (a) waveguide layout of the PLC and (b) detailed diagram of an interleaver. The PLC is 9.3 cm \times 2.6 cm. The evanescent couplers are nominally 50/50. The numbers tell the local path-length difference, λ_c being the design center wavelength.

It is well known that by combining multiple evanescent couplers one can make a coupler with a coupling ratio that is less sensitive to wavelength, polarization, and fabrication (WPF) variations. Both two^[3] and four-stage^[4] "desensitized" arrangements have been proposed. We propose here a three-stage arrangement, consisting of three identical, nominally 50/50 evanescent couplers connected by two differential delays, ϕ_1 and ϕ_2 . This three-stage arrangement is $\sim 30\%$ shorter and slightly lower loss than the four-stage one and yet is sufficiently desensitized for our application.

If the inputs to the coupler are u_1 and u_2 (the complex amplitudes of the fields), and the accumulated phase difference between the eigenmodes in each evanescent coupler is $\pi/2 + 2\Delta$, where $\Delta \ll 1$ and represents a error in the coupling ratio, then the outputs v_1 and v_2 are

$$\begin{bmatrix} v_1 \\ v_2 \end{bmatrix} = \frac{1}{2\sqrt{2}} \begin{bmatrix} 1-\Delta & j+j\Delta \\ j+j\Delta & 1-\Delta \end{bmatrix} \begin{bmatrix} e^{j\phi_1} & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} 1-\Delta & j+j\Delta \\ j+j\Delta & 1-\Delta \end{bmatrix} \begin{bmatrix} e^{j\phi_1} & 0 \\ 0 & 1 \end{bmatrix} \begin{bmatrix} 1-\Delta & j+j\Delta \\ j+j\Delta & 1-\Delta \end{bmatrix} \begin{bmatrix} u_1 \\ u_2 \end{bmatrix} \quad (1)$$

The nominal coupling ratio is

$$R = \frac{1}{8} |1 + e^{j\phi_1} - e^{j\phi_1} + e^{j\phi_1 + j\phi_2}|^2 \quad (2)$$

The sensitivity of R to Δ is minimized when

$$\begin{aligned}
 & [1 + \cos\phi_2 - \cos\phi_1 + \cos(\phi_1 + \phi_2)] - [1 - \cos\phi_2 - 3\cos\phi_1 - \cos(\phi_1 + \phi_2)] = \\
 & - [\sin\phi_2 - \sin\phi_1 + \sin(\phi_1 + \phi_2)] - [\sin\phi_2 - 3\sin\phi_1 - \sin(\phi_1 + \phi_2)]
 \end{aligned} \tag{3}$$

We thus have two equations, (2) and (3), for two variables, ϕ_1 and ϕ_2 . Some computer-found solutions are listed in Table 1.

Coupling ratio	ϕ_1	ϕ_2
50/50	0°	120°
75/25	116.9°	34.2°
90/10	110.1°	58.4°
100/0	90°	90°

Table 1. Some parameter choices for the proposed 3-stage coupler.

ϕ_1 and ϕ_2 can be interchanged and/or both multiplied by a minus sign without affecting the coupling ratio (e.g., 117°, 34° and -117°, -34° and 34°, 117° and -34°, -117° all give the same ratio). If only one angle is multiplied by a minus sign, then the coupling ratio flips (e.g., 117°, 34° gives a 75/25 ratio, whereas 117°, -34° gives a 25/75 ratio).

As is well known, Fourier-filter interleavers exhibit non-zero chromatic dispersion, which can be canceled by cascading two stages^[5,6]. In our case we adjust the MZI arm lengths in one of the interleavers to shift the wavelength response by half of the interleaver free-spectral range^[4]. Thus the net chromatic dispersion for the undropped channels is zero.

2. Results

The PLCs were made using 0.80% index-step silica waveguides on a silicon substrate. One was fully packaged with its own drivers on a circuit board. One phase shifter on one MZI arm of each of the stages of each interleaver were accessed via probe needles connected to voltage sources. These two voltages were adjusted so as to wavelength-align the interleaver to the WSC passbands and to optimize the crosstalk. For ~ 5 seconds, each voltage was increased to an extremely high value and then decreased, so as to trim via hyperheating^[7]. This process was repeated until both applied voltages became zero, leaving the interleaver permanently adjusted and passive.

The add-star coupler path transmissivities for all 8 inputs are shown in Fig. 3 with the VOAs set at 0- and 10-dB attenuation. The VOAs are operated push-pull^[8], and the polarization-dependent loss (PDL) of the entire add path over the 10-dB range is < 1.0 dB. To achieve such uniform, relatively low-loss performance, we used a symmetric star coupler (except for a port shift) with strong mutual coupling and focusing on the phase centers in the arrays^[9], along with segmentation^[10] and parallel inlet horn walls^[11].

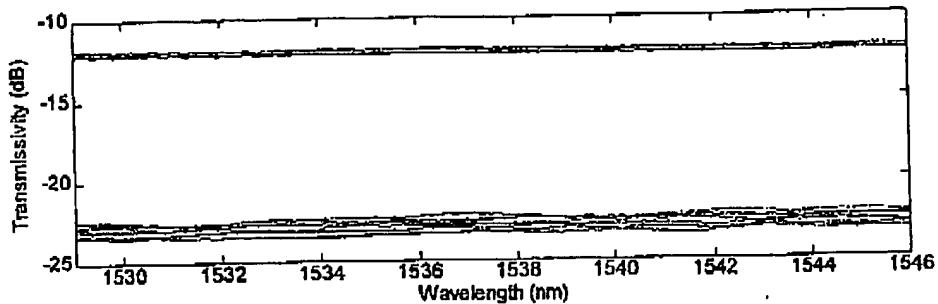


Fig. 3. Measured spectra of the eight add paths with VOAs at 0- and 10-dB attenuation. Fiber-to-fiber, including one connector (likewise for all subsequent plots).

The in-to-thru spectra of the WSC for three different configurations are shown in Fig. 4. The worst-case loss is < 4.75 dB, and the worst-case extinction ratio is > 55 dB. The thru shutters also act as VOAs, and the in-to-thru worst-case PDL at 0-dB and 12-dB attenuation are 0.1 and 0.6 dB, respectively. The in-to-drop spectra of the WSC for sending all 8 channels to each of the 8 drop ports in succession are shown in Fig. 5. The worst-case loss is < 7.5 dB, and the worst-case extinction ratio is > 43 dB. Shown overlaid are the spectra measured at the ports with only double rejection. To be sure that the extinction ratio is adequate for all 9^8 possible states of the WSC without measuring them all, we toggled each of the switches/shutters individually, with and without its neighbors activated (to account for thermal crosstalk) and measured the worst-case extinction ratio of each switch/shutter over all polarizations. We found that the worst-case extinction ratios of all 72 shutters are between 22.6 and 39.2 dB and of all 64 1 \times 2 switches, for both up and down states, are between 20.0 and 36.6 dB. Thus the worst possible crosstalk is 42.6 dB.

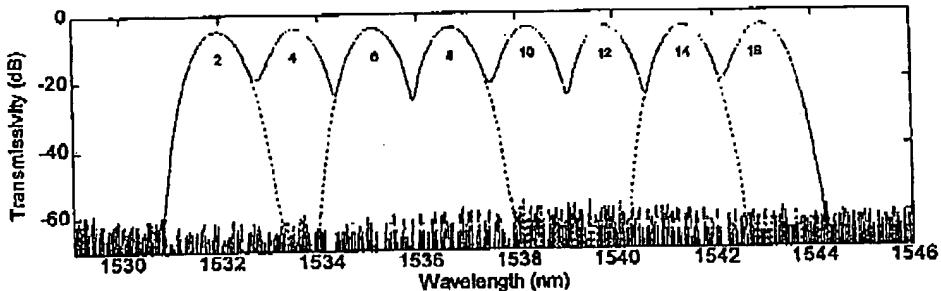


Fig. 4. Measured in-to-thru spectra of WSC for three cases overlaid: no channels dropped, all channels dropped, and only channels 4, 10, and 12 dropped.

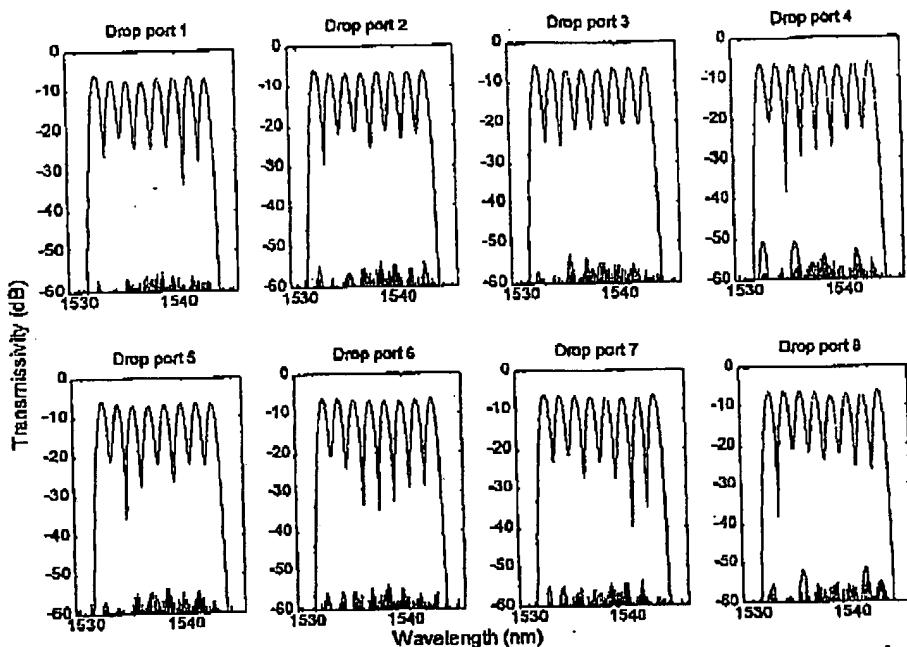


Fig. 5. Measured in-to-drop spectra of WSC for cases of sending all channels to each drop port for each figure. Overlaid are the measured spectra at the ports with only double crosstalk rejection.

The measured spectra of the interleavers are shown in Fig. 6. The loss ranges from 2.25 dB to 3.25 dB. The PDL is 0.1 dB. We then constructed the start-up node of Fig. 1a, leaving out the add path, by connecting together the appropriate fibers on the fiber-ribbon attached to the PLC. We chose the outer interleaver as the de-interleaver, because of its better crosstalk. The measured through-path and drop-path spectra are shown in Figs. 7 and 8, respectively. The worst-case through loss is < 10.8 dB. If the add coupler were added, and if it is a 50/50 fiber coupler, the total through loss for the node would be ~14 dB. The measured chromatic dispersion for the through path is shown in Fig. 9. The magnitude is < 8 ps/nm over the passband.

We thank M. Zirngibl for support and J. Fernandes for assistance.

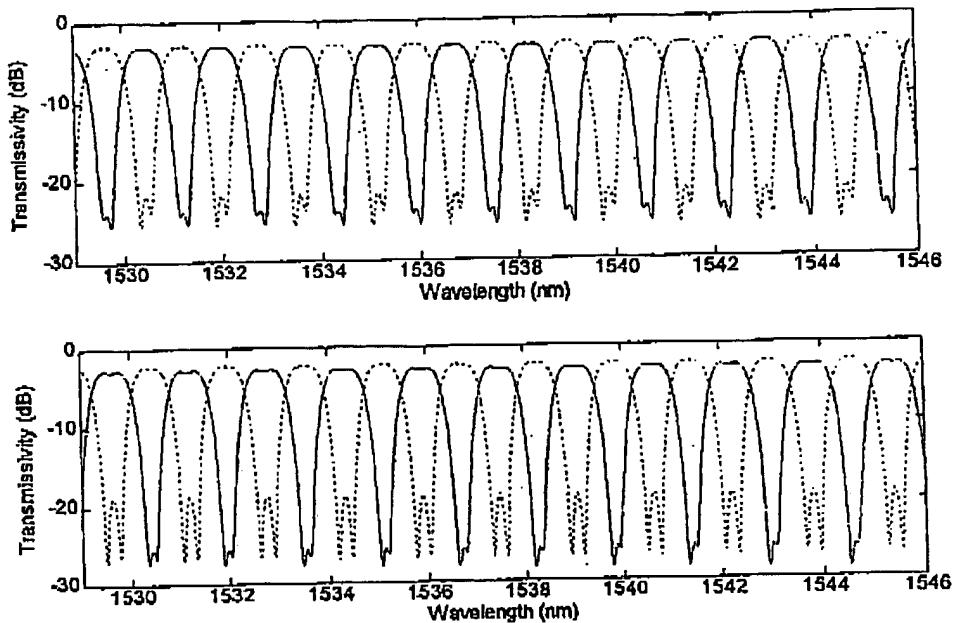


Fig. 6. Measured spectra of interleavers. Upper and lower plots are the outer and inner interleavers, respectively.

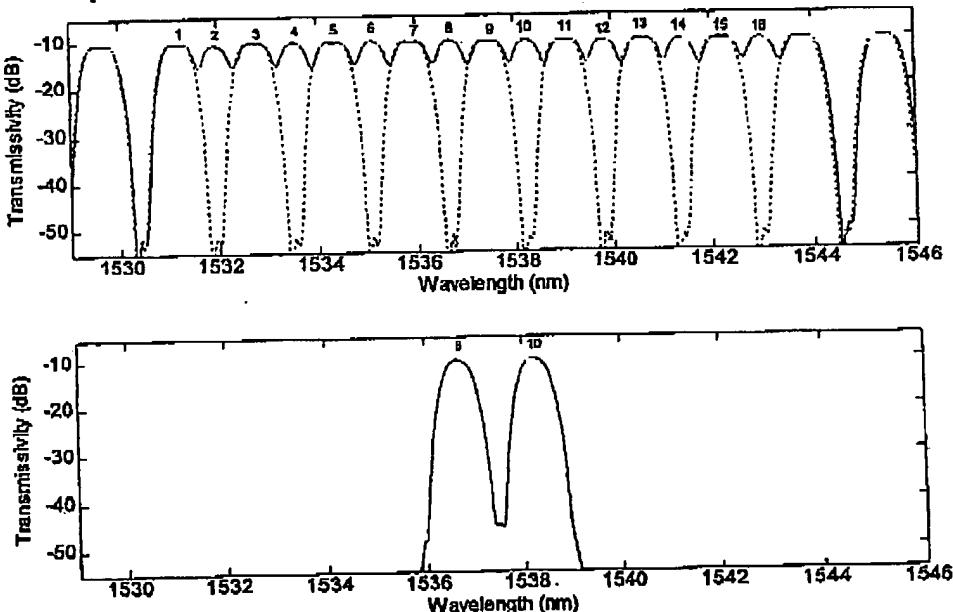


Fig. 7. Measured spectra of the start-up node. Upper plot is from input of de-interleaver to output of interleaver for the cases of no and all channels dropped. Lower plot is from input of de-interleaver to drop port 5 for the case of channels 8 and 10 dropped to drop port 5.

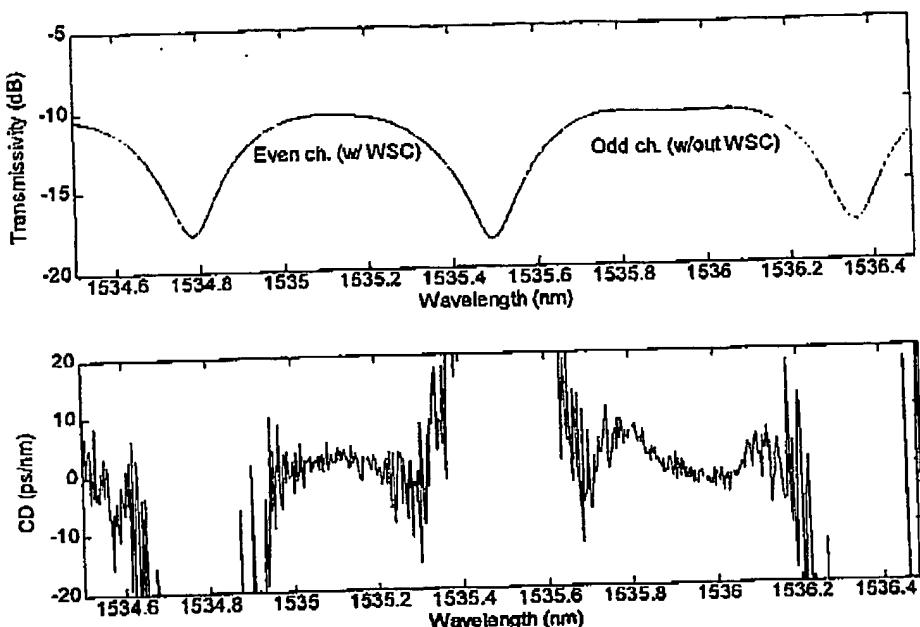


Fig. 8. Measured transmissivity and chromatic dispersion of through path of the start-up node.

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- ¹¹ C. R. Doerr, R. Pafchek, and L. W. Stulz, "16-band integrated dynamic gain equalization filter with less than 2.8-db insertion loss," *IEEE Photon. Technol. Lett.*, vol. 14, pp. 334-336, 2002.

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April 10, 2003

VIA PRIORITY MAIL

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Re: <u>IDS No.:</u>	125663	<u>Telephone No.</u>	<u>Fax No.</u>
<u>Managing Attorney</u>		(732) 949-6559	(732) 949-0102
David A. Sasso		<u>Telephone No.</u>	
<u>Secretary</u>		(732) 949-3179	
Sharon Lobosco			

Dear Eamon:

Enclosed please find the above-referenced patent submission. *This submission is related to IDS #125620 and IDS #125666. Please file the application by July 14, 2003.* If for any reason you cannot meet the filing date requested or need to change the filing date, you MUST notify the Managing Attorney (MA) as soon as possible.

This application will not be foreign filed

After the final claims have been drafted and you are therefore in a position to identify the inventors, please send ATTACHMENT 1 "Request for Case Name/Number" VIA FAX to Norma Davis at (732) 949-6410.

A copy of the proposed application is to be sent to the MA prior to execution of the Declaration and Assignment; likewise, all other substantive papers such as amendments, appeal briefs and the like are to be sent to the MA prior to filing. Please note, however, that the MA may not be in a position to review the application or other papers prior to filing, or may choose to conduct only a quality control review either now or at a later time. Accordingly, the ultimate responsibility for the timely filing as well as the quality and contents of the papers of this application and any resulting patent remains with you. If you do not receive specific instructions from the MA within five (5) business days from the date that the application or any subsequent papers were submitted to the MA, you are authorized and directed to transmit same directly to the USPTO.

If, during the prosecution of an application, you believe that a CPA/CIP, Divisional, Appeal, etc. should be filed, your advice should be presented to the MA for concurrence, prior to commencement of any work.

Very truly yours,

Susan E. Curry

SEC:nmd
Encl. (As above)

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APR 14 2003

MOSEN, PATTERSON
& SHERIDAN

 Tony Villabon

06/20/2003 03:57 PM

To: dslevy@lucent.com, grichards@lucent.com; larry@lucent.com
cc: (bcc: Carol Wilson/MPS)
Subject: Patent Applications LCNT/125620, LCNT/125663, LCNT/125666

Dear Inventors,

My name is Jorge Tony Villabon and I am an associate at Moser, Patterson & Sheridan, outside Patent Counsel for Lucent Technologies. We have been working on the above referenced three patent applications regarding a novel low-cost start-up add/drop node and we have a few questions regarding the inventive concepts of the invention that we need clarified.

Please contact me at your earliest convenience so that we may set up a telephone interview and briefly discuss the concepts of the invention.

We would appreciate any help that you can extend to us. Thank you very much in advance. We look forward to your reply.

Best Regards,

Jorge Tony Villabon, Esq.
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Tony Villabon
08/22/2003 12:52 PM

To: Chris Doerr <crdoerr@lucent.com>
cc: (bcc: Carol Wilson/MPS)
Subject: LCNT/125663 (Doerr 74) optical monitor

Dr. Doerr,

Attached is a first draft of a patent application written from a technical memorandum that you submitted to Lucent Technologies regarding a novel Optical Monitor. Please review the draft for technical accuracy. Also please answer the inventor questions that appear in bold text throughout the disclosure. Please keep in mind that a patent disclosure must teach one skilled in the art how to make and use the subject invention without "Undue Experimentation" so please be as detailed as you feel is necessary.

Please be sure to clearly indicate where any changes have been made (i.e., changes in red) so that I can incorporate your changes into a final draft.

Also attached herein is a copy of the informal Figures. Please provide a Figure 2 and a brief explanation as indicated in the text. Upon your approval, formal figures will be made for your patent application.

If you have any questions or need any assistance, please feel free to contact me directly at your convenience. Thank you very much for your time and consideration regarding this matter.

Best Regards,

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- LCNT_125663.DOC



125663figs.pdf

 Tony Villabon
09/04/2003 11:44 AM

To: Crdoerr@aol.com
cc: (bcc: Carol Wilson/MPS)
Subject: Re: 125663

Dr. Doerr,

Thank you so much for taking so much time to review all of the applications. Your comments were very helpful and appreciated. We are filing the 125620 and 125666 applications today pending the approval of David Sasso.

There only remains one last question with respect to the 125663 invention.

If you would like us to include this embodiment in your invention, please describe what sort of polarization correction means may be used. If not we will file the application as is. We just want to get the most protection for your invention.

Again, thank you very much for all of your time and consideration regarding all of these applications. You are a pleasure to work with.
I await your response. Thank you.

Best Regards,

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Subject: 125663

hi Chris

David Sasso asked me to email you the 125663 application and figures

thank you



- LCNT_125663.DOC



- LCNT125663figs (2).PDF

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